

IN THE FIGURES:

Please delete Figures 1-30b and replace with new Figures 1-30b attached hereto

Please add new Figures 31a – 46i attached hereto.

IN THE SPECIFICATIONS:

Please delete paragraph [0058] and replace it with new paragraph [0058] below:

--FIGS. 31—46 31a – 46i depict schematic diagrams of the presently preferred embodiments.--

Please delete paragraph [00132] and replace it with new paragraph [00132] below:

-- FIGS. 11, 12, 13, 14, 15 and 16 show how the auxiliary input signals 820, the voltage and current input signals 821, and the digital I/O signals 844 may be represented in the object oriented structure of this embodiments embodiment. In an exemplary embodiment, in the IED 900 the logic or code is implemented in firmware and in the PC the code is implemented in software. It will, of course, be recognized by those skilled in the art that the logic for the IED 900 can also be implemented in software and that the logic in the PC can be implemented in firmware. In the present embodiment, the firmware is implemented using a 512K byte flash EEPROM 834 available from Intel as a 28F010 EEPROM. In an exemplary embodiment, the software is written in the C programming language. An exemplary embodiment of the logic for the object oriented architecture of the present embodiments in object code is given in CD-ROM Appendix which is incorporated herein by reference and which corresponds to the code included in the microfiche appendix of U.S. Pat. No. 5,650,936, referred to above. The object code is presented in Srecord format which is defined in the M68332BUG Debug Monitor User's Manual (Motorola 1990) which is incorporated herein by reference. More detailed schematics for the presently preferred embodiment are given in Figures 31—46 31a - 46i.--